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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT G. Saito, et al.	
		FILING DATE September 10, 2003	EXPECTED GROUP 2825

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
1h1	AA	6,589,879	7/8/2003	Williams, et al.	438	714
1h1	AB	4,693,781	9/15/1987	Leung, et al.	156	643
1h1	AC	4,857,477	8/15/1989	Kanamori	437	47
1h1	AD	6,235,643	5/22/2001	Mui, et al.	438	719
1h1	AE	6,153,494	11/28/2000	Hsieh, et al.	438	424
1h1	AF	6,001,706	12/14/1999	Tan, et al.	438	424
1h1	AG	5,843,846	12/1/1998	Nguyen, et al.	438	713
1h1	AH	6,432,832	8/13/2002	Miller, et al.	438	713

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation/Abstracts	
						Yes	No
1h1	AI	2001-345375	12/14/2001	Japan	—	X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

1h1	AJ	S. Wolf, "Silicon Processing for the VLSI Era", Vol. II, Process Integration, 1990, pages 51-54					
1h1	AK	TI's "Trench Technology Moves in the Factory", Electronics, July 9, 1987, pages 75-87					
1h1	AL	R. N. Carlile, et al. "Trench Etches in Silicon with Controllable Sidewall Angles", Electrochem. Soc. Solid-State Science and Technology, August 1988, pages 2058-2064					
1h1	AM	G. K. Herb, et al., "Silicon Trench Etch in a Hex Reactor", Solid State Technology, October 1987, pages 109-115					

Examiner 	Date Considered 8/2/04
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